

PRELIMINARY AMENDMENT

Serial Number: 10/761,995

Filing Date: January 21, 2004

Title: CACHE LINE PRE-LOAD AND PRE-OWN BASED ON CACHE COHERENCE SPECULATION

Assignee: Intel Corporation

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Dkt: 884.220US2 (INTEL)

REMARKS

Claims 1 and 10 have been amended.

It is believed that no new matter has been added by this amendment.

The Examiner is invited to contact Applicant's representative at (612) 349-9581 if prosecution may be assisted thereby.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully Submitted,

JIH-KWON PEIR ET AL.

By their Representatives,

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Date Oct 11 '05

By 

John M. Dahl

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CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 11th day of October 2005.

Name Amy Moriarty

Signature 